

I claim:

1. A key inputting circuit while pressing one of keys then an input reference voltage signal will have a predetermined voltage drop caused by a specific number of series resistances and a designated voltage signal will output to a CPU.

2. The key inputting circuit according to claim 1 further comprises a potential comparator which is to compare said reference voltage signal and said designated voltage signal from said key inputting circuit and decide to enable said CPU or not.

3. The key inputting circuit according to claim 1 further comprises an A/D converter which is to convert said designated voltage signal to digital unit and transmits to said CPU.

4. The key inputting circuit according to claim 1, wherein said resistances are selected from the group consisting of carbon-film resistor, metal-film resistor, resistor network, and chip resistor.

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5. A key inputting circuit comprising :

a load resistance connected between a reference voltage source and a signal gathering terminal ; and

a key inputting module connected between said signal gathering terminal and ground which is formed by a plurality of series resistances and a plurality of keys wherein one end of every said key are connected to said signal gathering terminal and the other end of every said key are individually connected to corresponding series connection nodes of said series resistances ;

wherein one of the keys of said key inputting module is pressed and a predetermined voltage drop is caused by said load resistance and said

series resistances corresponding to said pressed key then a designated voltage signal will be outputted from said signal gathering terminal for a CPU to determine the value corresponding to said pressed key.

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6. The key inputting circuit according to claim 5 further comprises a potential comparator which is to compare said reference voltage signal and said designated voltage signal from said key inputting circuit and decides to enable said CPU or not.

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7. The key inputting circuit according to claim 5 further comprises an A/D converter which is to convert said designated voltage signal to digital unit and transmits to said CPU.

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8. The key inputting circuit according to claim 5, wherein said resistances are selected from the group consisting of carbon-film resistor, metal-film resistor, resistor network, and chip resistor.

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9. An electronic device includes at least two circuit boards, a first circuit board and a second circuit board, and a CPU is mounted on said first circuit board, said electronic device comprising :

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a key inputting module which is arranged on said second circuit board, as soon as one of keys is pressed then an input reference voltage signal will have a predetermined voltage drop caused by a specific number of series resistances and a first voltage signal will be outputted to said CPU ; and

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a potential comparator which is to compare said reference voltage signal and said first voltage signal from said key inputting module and to output a second voltage signal thereby decide to enable said CPU or not ;

wherein said first voltage signal and said second voltage signal can be transmitted through said first circuit board and said second circuit board thereby complete the communication between said key inputting module and said CPU.

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10. The electronic device according to claim 9 further comprises an A/D converter which is to convert said first voltage signal to digital unit and transmit to said CPU.

10 11. The electronic device according to claim 9, wherein said resistances are selected from the group consisting of carbon-film resistor, metal-film resistor, resistor network, and chip resistor.

12. The electronic device according to claim 9, wherein said electronic
15 device is a communication device.

13. The electronic device according to claim 9, wherein said electronic device is a mobile phone.